

REMARKS


Embodiments of the present invention are directed to a multiple level memory. In a multilevel memory, more than one bit may be stored per cell. However, in all multilevel memories before the present application, the same number of bits were stored per cell across the array.

Lu is nothing but the conventional multilevel memory. He never contemplated, suggested, or explained how to store more than one density (number of bits per cell) in the same array. Thus, he does not teach using multilevel cells with two bits per cell in one place and three bits per cell in another place, to give a purely hypothetical example. Instead, he simply describes the concept of multilevel bits.

Therefore, reconsideration is respectfully requested.

Respectfully submitted,

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